



UNIVERSITI PUTRA MALAYSIA

***A MODIFIED APPROACH TO IMPROVE THE PERFORMANCE OF AES
USING FEISTEL STRUCTURE***

AFEEF YAHYA AHMED AL-ANSI

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By
AFEEF YAHYA AHMED AL-ANSI

Thesis Submitted to the School of Graduate Studies, Universiti Putra
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of Information Security
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DEDICATIONS

To soul of my mother.

To my great father.

To my beloved wife.

To my children .. Ayman, Aisha and Ayoub.

To my brothers and sisters.

To all my friends.

To each of who has taught me or gave me advice.. teachers.

Dedicate this work. . .

Abstract of thesis presented to the Senate of University Putra Malaysia in
fulfillment of the requirement for the degree of Master of Information Security

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January 2018

Chair: Assoc. Prof Dr. Zurina Mohd Hanapi, Ph.D.

Faculty: Computer Science and Information Technology

In encryption algorithm design, apart from the security performance, processing performance and the cost of the implementation are very important trade-off parameters. A most popular and widely adopted symmetric encryption algorithm is the Advanced Encryption Standard (AES). It suffers from the demand for the performance efficiency. To improve its computational cost, we propose a modification of the AES technique. Having found that out of the four major operations in the AES; MixColumn is the one that takes huge amount of computing time and for which replacement with look-up table adds additional space constrain, we propose replacing the MixColumns with a Feistel Structure that exists and is the main engine of the Data Encryption Standard (DES). Empirical performance analysis on our proposed modified AES shows a significant reduction in the processing time by up to 67% on the average. The method support parallel implementation with little overhead.

Abstrak tesis yang dikemukakan kepada Senat Universiti Putra Malaysia sebagai
memenuhi keperluan untuk ijazah Master keselamatan maklumat

**PENDEKATAN YANG DIUBAH SUAI BAGI MENINGKATKAN PRESTASI AES
MENGUNAKAN STRUKTUR FEISTEL**

Oleh

AFEEF YAHYA AHMED AL-ANSI

Januari 2018

Pengerusi: Prof. Madya Dr. Zurina Mohd Hanapi, Ph.D.

Fakulti: Fakulti Sains Komputer dan Teknologi Maklumat

Dalam reka bentuk algoritma, selain daripada prestasi keselamatan, prestasi pemprosesan dan kos pelaksanaan adalah parameter pertimbangan yang sangat penting. Algoritma simetri yang paling popular dan digunakan secara meluas adalah Advanced Encryption Standard (AES). Namun ia menderita akibat kehendak permintaan untuk kecekapan. Untuk meningkatkan kos pengiraannya, kami mencadangkan pengubahsuaian teknik AES. Setelah mendapati bahawa daripada empat operasi utama dalam AES, MixColumn adalah salah satu yang mengambil masa pengkomputeran yang besar dan mengurangkan ruang, maka, yang mana penggantian dengan jadual paparan menambah ruang; maka kami mencadangkan penggantian MixColumns dengan Struktur Feistel, yang sedia ada, dan merupakan enjin utama dalam Data Encryption Standard (DES). Analisis prestasi empirik terhadap AES yang diubahsuai menunjukkan pengurangan yang signifikan dalam masa pemprosesan sehingga purata 67%. Kaedah ini menyokong pelaksanaan selari, dengan kos tetap yang sedikit.

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I certify that a Thesis Examination Committee has met on (January 19, 2018) to conduct the final examination of (AFEEF YAHYA AHMED AL-ANSI) on thesis entitled “A modified approach to improve the performance of AES using Feistel structure” in accordance with the Universities and University Colleges Act 1971 and the Constitution of the Universiti Putra Malaysia [P.U.(A) 106] 15 March 1998. The Committee recommends that the student be awarded the (Master of Information Security).

Members of the Thesis Examination Committee were as follows:

Zurina Mohd Hanapi, Ph.D.

Assoc. Prof. Dr.

Computer Science and Information Technology
Universiti Putra Malaysia

Sharifah Md. Yasin, Ph.D.

Dr

Computer Science and Information Technology
Universiti Putra Malaysia
(Internal Examiner)

DECLARATION

I declare that the thesis is my original work except for quotations and citations which have been duly acknowledged. I also declare that it has not been previously, and is not concurrently, submitted for any other degree at University Putra Malaysia or at any other institution.



(Signature)

AFEEF YAHAY A. AL-ANSI
Date: JANUARY 23, 2018

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LIST OF ABBREVIATIONS

Abbreviation	Meaning
ASIC	Application-Specific Integrated Circuit
BRAM	Block Random Access Memory
CBC	Cypher Block Chaining
CIA	Confidentiality Integrity and Availability
CFB	Cipher FeedBack
CTR	Counter
CUDA	Computing Unified Device Architecture
DES	Data Encryption Standard
ECB	Electronic Code Book
FPGA	Field Programmable Gate Array
GF	Galois Field
GPU	Graphic Processing Unit
IDEA	International Data Encryption Algorithm
IV	Initialization Vector
LSW	Least Significant Word
NIST	National Institute of Standards and Technology
OFB	Output FeedBack
PKC	Public-Key Cryptography
RC4	Rivest Cipher Four
PRN	Pseudo-Random Numbers
S-box	Substitution Box
SKC	Symmetric-Key Cryptography
SPN	Substitution-Permutation Network
SCA	Side-Channel Attack
XOR	Exclusive Or

CHAPTER 1

INTRODUCTION

1.1 Background of the Study

There are three main goals to ensure system security: Confidentiality of the information stored and/or shared; Integrity of the system and its associated data; and the system Availability as at when needed (Perrin, 2008). These cardinal goals are often refers to as Confidentiality, Integrity and Availability (CIA) triad. To achieve the Confidentiality and Integrity, cryptography is a main techniques on which the security protocols are built (Almorsy, Grundy & Müller, 2016). Cryptographic techniques are classified into two, based on the keying type: symmetric-key cry cryptography (SKC) that utilizes single key for both of the encryption and decryption process and asymmetric, also known as public-key cryptography (PKC) in which each of the process utilizes different key. The symmetric (also called private key) cryptography could operate on block form or on character stream of information. Some of the private key cryptography includes data encryption standard (DES) (FIPS, 1999), Advance Encryption Standard (AES) (NSIT, 2001), Blowfish (Schneier, 1993), the International Data Encryption Algorithm (IDEA) (Leong, Cheung, Tsoi & Leong, 2000) and Rivest Cipher Four (RC4) (Mousa & Hamad, 2006). DES was de facto among them. AES being an advancement of the DES is considered more secured due to utilizing large key sizes, and is suitable for both 8-bit microprocessor platforms and 32-bit processors (Daemen & Rijmen, 2013). Thus, as standardized by the National Institute of Standards and Technology (NIST), it is accepted as an appropriate replacement for DES.

In the encryption algorithm design, apart from the security performance, the algorithm efficiency is a significant factor that is given serious consideration by researchers. The cost of the implementation is very important because of the varying applications/platform on which the algorithm are deployed, such as embedded systems, sensor networks, e-commerce, banking, and online transaction processing applications. AES remains the most popular and widely adopted symmetric encryption algorithm.

In the AES, all computations are performed on bytes rather than bits. Hence, AES treats the 128 bits of a plaintext block as 16 bytes. These 16 bytes are arranged in four columns and four rows of bytes for processing as a matrix. The matrix undergoes rounds of four sets transformations. Among which is what is referred to as MixColumns transformation. Even as AES is efficient, the MixColumn process involves a huge computing overheads in the whole transformation, for which a lookup table substitute also leads to additional large space overhead (Daemen & Rijmen, 2013).

1.2 Problem Statement

In general, apart from its security, various works on cryptography tries to overcome varied various performance challenges such as execution time (throughput), memory requirement and system's computation power. The power consumption depend mainly on the two other metrics. Classic AES and latest modified AES (Fei et al. 2016) are taken too much resources that lead to poor processes performance.

The major factor that determines speed of the implementations comes from the four basic building blocks operations in the Substitution-Permutation Network (SPN) of the AES – the RowShift, MixColumn, the AddRoundKey and the SubBytes. Among them the MixColumn is the most computationally involved symmetric operation for which table substitution also leads to large additional space requirement (Kawle et al. 2014).

Considering the essence of the MixColumn operation, replacing it with a similar but more efficient Feistel structure in the DES network, to suit the operations in the AES, could greatly improve the implementation efficiency in terms of processing time. In addition, adopting parallel operation in the cryptographic process is also of great benefit in boosting the efficiency. We consider the Feistel network also suitable for parallelizing the implementation.

1.3 Study Objectives

The main objective of this research is to improve the AES performance by reducing the computation overhead in the implementation. This, we expect to achieve by achieving the following objectives:

- i. To propose a new AES implementation by integrating, , Feistel Network as a substitution to the more computationally involved MixColumn operation, in order to improve the performance over the classic implementation;
- ii. To analyze the performance of parallel implementation of the proposed AES in order to improve further the processing time.

1.4 Scope and Limitation of the Study

In this project, we optimize the efficiency of the AES implementation in term of processing time. We integrate into the SPN, a modified Feistel Network of the DES to achieve computing time, and parallelization technique to further enhance the computing efficiency.

We use processing time to measure the AES algorithm performance. We have not explicitly considered the computational security.

We implement a fully functional algorithm that takes any kind of file, using C, for the proposal.

The main performance metric we considered here is execution time in the form of throughput. The space utilization comes from the elimination of any lookup table for MixColumn. As in the original AES, the design of the proposed modification is also intended to be scalable (or even better) with both 8-bits and 32-bit processors. The main target in the parallelization is software implementation

1.5 Thesis organization

This project is organized into the following five chapters.

Chapter 1 establishes the background of the study. The problem studied is stated and the objectives to be achieved is also enumerated. The Chapter ends with detailed scope and limitation of this project.

In Chapter 2, begins with background on AES and its implementation. Literature related to symmetric key encryption in general and specific to AES implementation is critically reviewed. The Chapter concludes with identification of the limitations of the current works on the implementation and proffers means of addressing the limitation.

Chapter 3 detailed the methodology adopted in this project.

In Chapter 4, we discuss the results from the new method implantation and the parallelization carried out. The implication of the result in the real AES is evaluated and discussed.

In Chapter 6, we conclude the project and recommend on future enhancements of this work.

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