

**FAIR TRAFFIC SCHEDULING AND SHAPING ALGORITHMS FOR
DIFFSERV NETWORKS**

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**DOCTOR OF PHILOSOPHY
UNIVERSITY PUTRA MALAYSIA**

2006

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DIFFSERV NETWORKS**

By

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**Thesis Submitted to the School of Graduate Studies, University Putra Malaysia, in
Fulfilment of the Requirements for the Degree of Doctor of Philosophy**

July 2006

**This work is dedicated to my beloved
Father, Mother, Brothers, Sisters and my
family Raja, Waad and Sajid**

**Abstract of thesis presented to the Senate of Univesiti Putra Malaysia
in fulfillment of the requierments for the degree of Doctor of Philosophy**

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December 2006

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Traditionally, the Internet has provided only best effort service to every user without any consideration to any requirements. As the Internet grows and becomes universally available, it becomes very important to deal with real time service delivery to application such as IP telephony. Thus, an interest has developed in having the Internet to provide some degree of Quality of Service (QoS). To provide different QoS commitments, the IETF developed many technologies that requires resources such as bandwidth and buffers to be explicitly reserved for a given data flow to ensure that the application receives its requested QoS.

DiffServ is proposed by the thought that per-flow moved to the edge of the network with very simple functionalities left at the core network. However, services provided

by DiffServ networks have lower flexibility and fairness among the aggregates sharing the network and among the flows of those aggregates. Per Hop Behaviours (PHBs) are implemented at DiffServ networks nodes using some scheduling and queuing mechanisms that are predecessor by markers and shaper. The current proposed DiffServ have unfairness problem that is caused by these elements. This thesis investigates the effect of different scheduling mechanisms and usage of a different marker algorithms then develop and implement a fair scheduling mechanism and integrate it with a shaper with the usage of simulation and analytical techniques.

In this thesis, I²tswTCM is proposed as an enhanced version of the famous tswTCM marker scheme used in the current DiffServ architecture, this new algorithm improves fairness in the excess bandwidth among different aggregates in a DiffServ networks and it has a better fairness than all other algorithms for different network provision levels. In addition, the algorithm marks the right amount of yellow traffic into the network proportional to the CIR. Nevertheless, it is not sensitive to the number of flows enters the networks. The thesis also proposed a new scheduling algorithm (FWFQ) which suite the DiffServ architecture and it gives a better performance in terms of delay and jitter. The research shows the importance of considering the scheduler type when dealing with different types of traffic sources in DiffServ networks. The interaction of different traffic type flows in a DiffServ networks using the proposed integration model between FWFQ and the dual token

bucket shaper algorithm is analyzed. The result shows that the proposed model can significantly improve the performance.

**Abstrak tesis yang dikemukakan kepada Senat Universiti Putra Malaysia
sebagai memenuhi keperluan untuk ijazah Doktor Falsafah**

**ALGORITMA PENSKEDULAN DAN PEMBENTUKAN TRAFIK ADIL UNTUK
RANGKAIAN-RANGKAIAN DIFFSERV**

Oleh

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Secara tradisi, Internet menawarkan perkhidmatan terbaik kepada setiap pengguna tanpa mengambil kira sebarang pertimbangan terhadap ke perluan. Apabila Internet berkembang dan digunakan secara meluas, ia menjadi penting untuk menangani penghantaran perkhidmatan secara nyata ke aplikasi seperti telefon *IP*. Lanjutan itu, tercetusnya minat untuk *Internet* menawarkan sesuatu peringkat perkhidmatan berkualiti (QoS). Untuk menawarkan kesungguhan *QoS* yang berbeza, IETF telah membangunkan banyak teknologi yang berasaskan sumber seperti jalur lebar dan storan sementara dikhaskan untuk aliran data yang diberi bagi memastikan aplikasi tersebut menerima *QoS* yang diperlukan.

DiffServ dicadangkan kerana idea operasi pra aliran harus digerakkan ke hujung rangkaian-rangkaian dan fungsi yang mudah dibiarkan dalam rangkaian utama.

Walaubagaimana pun, perkhidmatan yang ditawarkan oleh rangkaian- rangkaian *DiffServ* mempunyai keanjalan yang rendah dan adil di antara perkongsian agregat dalam rangkaian dan di antara aliran agregat tersebut. *Per Hop Behaviors* (PHBs) dilaksanakan pada nod rangkaian-rangkaian *DiffServ* menggunakan penjadualan dan mekanisma barisan yang menjadi *predecessor* oleh penanda dan pembentuk. Cadangan *DiffServ* yang terkini mempunyai masalah ketidakadilan yang disebabkan oleh unsur-unsur tersebut. Tesis ini mengkaji kesan mekanisma penjadualan yang berbeza dan penggunaan algoritma penanda yang berbeza untuk membangunkan dan melaksanakan mekanisma yang berkesan dan mudah dan diintegrasikan dengan penajam yang menggunakan penanda yang adil dan saksama di penghujung rangkaian-rangkaian *DiffServ* dengan bantuan simulasi dan teknik penganalisaan.

Dalam kajian ini, $I^2tswTCM$ dicadangkan sebagai versi tambahan untuk skema penanda *tswTCM* yang terkenal dengan menggunakan rekabentuk semasa. Algorithma baru ini memperbaiki keadilan lebar jalur yang berlebihan di antara agregat yang berlainan dalam rangkaian-rangkaian *DiffServ*. Ia mempunyai keadilan yang lebih baik daripada algorithma yang lain untuk lapisan rangkaian yang berbeza. Tambahan pula, algoritma ini memasukkan jumlah trafik kuning yang betul ke dalam rangakain memadani *CIR*. Namun begitu, ianya tidak sensitif ke atas jumlah aliran yang memasuki rangkaian. Tesis ini juga mencadangkan satu algorithma penjadualan baru (*FWFQ*) yang sesuai dengan rekabentuk *DiffServ* dan ia memberikan kesan yang lebih baik dalam penangguhan dan penangguhan tak

sekata (*jitter*). Kajian ini juga menunjukkan kepentingan untuk mempertimbangkan jenis penskedulan apabila menangani jenis sumber trafik yang berbeza dalam rangkaian-rangkaian *DiffServ*. Analisa dilakukan ke atas interaksi jenis aliran trafik yang berbeza dalam rangkaian-rangkaian *DiffServ* yang menggunakan model integrasi yang dicadangkan di antara *WFQ* dan kaedah pembentuk *dual token bucket*. Keputusan menunjukkan bahawa model yang dicadangkan boleh meningkatkan prestasi secara ketara.

ACKNOWLEDGEMENTS

All Praise is due to Almighty Allah as He is all Mercifull, Most Gracious and Most Compassionate who gathered all knowledge in His Essence and who is the Creator of all knowledge for eternity. May God praise our Prophet Muhammad, and render him and his household safe and secure from all evil. All Thanks belong to Almighty Allah.

I would like to thank the government of Malaysia and the University Putra Malaysia for offering me the chance of gaining knowledge in this country. Then my gratitude goes to Sudan University of Science and Technology (SUST).

I wish to express a deep appreciation and gratitude to my advisor and supervisor Associate Professor Dr. Mohamed Othman for his extensive help and valuable comments and advice at every stage of my research.

Further I must record my thanks to my co-supervisors Dr. Shamala Subramaniam and Dr. Jalil Md. Desa for their valuable help in making this research a reality.

I would also like to thank my examination committee members for their time and suggestions.

I should not forget the helpful discussions with my colleagues especially network research laboratory members.

I express warm appreciation to my teachers throughout my whole academic life.

I reserve my deepest gratitude to my parents, brothers, sisters and other family members for their preserving efforts towards my entire life.

Last but not least I reserve my deepest gratitude to my wife Raja, and my children Waad and Sajid for their preserving efforts towards my social and academic life.

I certify that an Examination Committee has met on 19 December 2006 to conduct the final examination of Mohamed Awad Elshaikh on his Doctor of Philosophy thesis entitled "Fair Traffic Scheduling and Shaping Algorithms for DiffServ Networks" in accordance with Universiti Pertanian Malaysia (Higher Degree) Act 1980 and Universiti Pertanian Malaysia (Higher Degree) Regulations 1981. The Committee recommends that the candidate be awarded the relevant degree. Members of the Examination Committee are as follows:

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DECLARATION

I hereby declare that the thesis is based on my original work except for quotations and citations which have been duly acknowledged. I also declare that it has not been previously or concurrently submitted for any other degree at UPM or other institutions.

MOHAMED AWAD ELSHAIKH

Date:

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LIST OF ABBRIVIATION

ACE	Accelerated
AF	Assured Forwarding
AUX	Auxiliary
BA Classifier	Behavior Aggregate Classifier
BE	Best Effort
CBS	Committed Bursty Size
CIR	Committed Information Rate
CIR_th	Committed Information Rate_threshold
DiffServ	Differentiated Services
DSCP	Differentiated Services Code Point
EBS	Excess Bursty Size
EF	Expedited Forwarding
FI	Fairness Index
FWFQ	Fair Weighted Fair Queue
GPS	Generalize Processor Sharing
HoL	Head of Line
HoL_PJ	Head of Line with Priority Jumping
I²tswTCM	Double Improve Time Sliding Window Three Color
IETF	Market Engineering Task Force
IID	Independent and Identically Distribution
ItswTCM	Improve Time Sliding Window Three Color Marker
MF Classifier	Multi-Field Classifier

MIR	Maximum Information Rate
MIR_th	Maximum Information Rate_threshold
MLT	Maximum Laxity Threshold
PBS	Peak Bursty Size
PEC	Performance Evaluation Checklist
PHB	Per Hob Behaviour
PIR	Peak Information Rate
PQ	Priority Queuing
PQWRR	Priority Queue Weighted Round Robin
QoS	Quality of Services
RAS	Rate Adaptive Shaper
RED	Random Early Detection
RSVP	Resource Reservation Protocol
RTT	Round Trip Time
SLA	Service Level Agreement
SLS	Service Level Specification
srRAS	Single Rate - Rate Adaptive Shaper
srTCM	Single Rate Three Color Marker
TD²FQ	Token Driven Delay-Sensitive Dynamic Fair Queuing
ToS	Type of Service
trTCM	Two Rate Three Color Marker
trRAS	Two Rate – Rate Adaptive Shaper
tswTCM	Time Sliding Window Three Color Marker

VC	Virtual Clock
WFQ	Weighted Fair Queue
WIRR	Weighted Interleave Round Robin
WRR	Weighted Round Robin