

**FAIR TRAFFIC SCHEDULING AND SHAPING ALGORITHMS FOR  
DIFFSERV NETWORKS**

**MOHAMED AWAD ELSHAIKH MOHAMED**

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**FAIR TRAFFIC SCHEDULING AND SHAPING ALGORITHMS FOR  
DIFFSERV NETWORKS**

**By**

**MOHAMED AWAD ELSHAIKH MOHAMED**

**Thesis Submitted to the School of Graduate Studies, University Putra Malaysia, in  
Fulfilment of the Requirements for the Degree of Doctor of Philosophy**

**July 2006**

**This work is dedicated to my beloved  
Father, Mother, Brothers, Sisters and my  
family Raja, Waad and Sajid**

**Abstract of thesis presented to the Senate of Univesiti Putra Malaysia  
in fulfillment of the requierments for the degree of Doctor of Philosophy**

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**MOHAMED AWAD ELSHAIKH**

**December 2006**

**Chairman : Aossociate Professor Mohamed Othman, PhD**

**Faculty : Computer Science and Information Technology**

**Traditionally, the Internet has provided only best effort service to every user without any consideration to any requirements. As the Internet grows and becomes universally available, it becomes very important to deal with real time service delivery to application such as IP telephony. Thus, an interest has developed in having the Internet to provide some degree of Quality of Service (QoS). To provide different QoS commitments, the IETF developed many technologies that requires resources such as bandwidth and buffers to be explicitly reserved for a given data flow to ensure that the application receives its requested QoS.**

**DiffServ is proposed by the thought that per-flow moved to the edge of the network with very simple functionalities left at the core network. However, services provided**

by DiffServ networks have lower flexibility and fairness among the aggregates sharing the network and among the flows of those aggregates. Per Hop Behaviours (PHBs) are implemented at DiffServ networks nodes using some scheduling and queuing mechanisms that are predecessor by markers and shaper. The current proposed DiffServ have unfairness problem that is caused by these elements. This thesis investigates the effect of different scheduling mechanisms and usage of a different marker algorithms then develop and implement a fair scheduling mechanism and integrate it with a shaper with the usage of simulation and analytical techniques.

In this thesis, I<sup>2</sup>tswTCM is proposed as an enhanced version of the famous tswTCM marker scheme used in the current DiffServ architecture, this new algorithm improves fairness in the excess bandwidth among different aggregates in a DiffServ networks and it has a better fairness than all other algorithms for different network provision levels. In addition, the algorithm marks the right amount of yellow traffic into the network proportional to the CIR. Nevertheless, it is not sensitive to the number of flows enters the networks. The thesis also proposed a new scheduling algorithm (FWFQ) which suite the DiffServ architecture and it gives a better performance in terms of delay and jitter. The research shows the importance of considering the scheduler type when dealing with different types of traffic sources in DiffServ networks. The interaction of different traffic type flows in a DiffServ networks using the proposed integration model between FWFQ and the dual token

**bucket shaper algorithm is analyzed. The result shows that the proposed model can significantly improve the performance.**

**Abstrak tesis yang dikemukakan kepada Senat Universiti Putra Malaysia  
sebagai memenuhi keperluan untuk ijazah Doktor Falsafah**

**ALGORITMA PENSKEDULAN DAN PEMBENTUKAN TRAFIK ADIL UNTUK  
RANGKAIAN-RANGKAIAN DIFFSERV**

**Oleh**

**MOHAMED AWAD ELSHAIKH**

**Disember 2006**

**Pengerusi : Professor Madya Mohamed Othman, PhD**

**Fakult : Komputer Sains dan Teknologi Maklumat**

Secara tradisi, Internet menawarkan perkhidmatan terbaik kepada setiap pengguna tanpa mengambil kira sebarang pertimbangan terhadap ke perluan. Apabila Internet berkembang dan digunakan secara meluas, ia menjadi penting untuk menangani penghantaran perkhidmatan secara nyata ke aplikasi seperti telefon *IP*. Lanjutan itu, tercetusnya minat untuk *Internet* menawarkan sesuatu peringkat perkhidmatan berkualiti (QoS). Untuk menawarkan kesungguhan *QoS* yang berbeza, IETF telah membangunkan banyak teknologi yang berasaskan sumber seperti jalur lebar dan storan sementara dikhaskan untuk aliran data yang diberi bagi memastikan aplikasi tersebut menerima *QoS* yang diperlukan.

*DiffServ* dicadangkan kerana idea operasi pra aliran harus digerakkan ke hujung rangkaian-rangkaian dan fungsi yang mudah dibiarkan dalam rangkaian utama.

Walaubagaimana pun, perkhidmatan yang ditawarkan oleh rangkaian- rangkaian *DiffServ* mempunyai keanjalan yang rendah dan adil di antara perkongsian agregat dalam rangkaian dan di antara aliran agregat tersebut. *Per Hop Behaviors* (PHBs) dilaksanakan pada nod rangkaian-rangkaian *DiffServ* menggunakan penjadualan dan mekanisma barisan yang menjadi *predecessor* oleh penanda dan pembentuk. Cadangan *DiffServ* yang terkini mempunyai masalah ketidakadilan yang disebabkan oleh unsur-unsur tersebut. Tesis ini mengkaji kesan mekanisma penjadualan yang berbeza dan penggunaan algoritma penanda yang berbeza untuk membangunkan dan melaksanakan mekanisma yang berkesan dan mudah dan diintegrasikan dengan penajam yang menggunakan penanda yang adil dan saksama di penghujung rangkaian-rangkaian *DiffServ* dengan bantuan simulasi dan teknik penganalisaan.

Dalam kajian ini,  $I^2tswTCM$  dicadangkan sebagai versi tambahan untuk skema penanda *tswTCM* yang terkenal dengan menggunakan rekabentuk semasa. Algorithma baru ini memperbaiki keadilan lebar jalur yang berlebihan di antara agregat yang berlainan dalam rangkaian-rangkaian *DiffServ*. Ia mempunyai keadilan yang lebih baik daripada algorithma yang lain untuk lapisan rangkaian yang berbeza. Tambahan pula, algoritma ini memasukkan jumlah trafik kuning yang betul ke dalam rangakain memadani *CIR*. Namun begitu, ianya tidak sensitif ke atas jumlah aliran yang memasuki rangkaian. Tesis ini juga mencadangkan satu algorithma penjadualan baru (*FWFQ*) yang sesuai dengan rekabentuk *DiffServ* dan ia memberikan kesan yang lebih baik dalam penangguhan dan penangguhan tak

sekata (*jitter*). Kajian ini juga menunjukkan kepentingan untuk mempertimbangkan jenis penskedulan apabila menangani jenis sumber trafik yang berbeza dalam rangkaian-rangkaian *DiffServ*. Analisa dilakukan ke atas interaksi jenis aliran trafik yang berbeza dalam rangkaian-rangkaian *DiffServ* yang menggunakan model integrasi yang dicadangkan di antara *WFQ* dan kaedah pembentuk *dual token bucket*. Keputusan menunjukkan bahawa model yang dicadangkan boleh meningkatkan prestasi secara ketara.

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**I certify that an Examination Committee has met on 19 December 2006 to conduct the final examination of Mohamed Awad Elshaikh on his Doctor of Philosophy thesis entitled "Fair Traffic Scheduling and Shaping Algorithms for DiffServ Networks" in accordance with Universiti Pertanian Malaysia (Higher Degree) Act 1980 and Universiti Pertanian Malaysia (Higher Degree) Regulations 1981. The Committee recommends that the candidate be awarded the relevant degree. Members of the Examination Committee are as follows:**

**Ali Mamat, PhD**  
**Associate Professor**  
**Faculty of Graduate Studies**  
**Universiti Putra Malaysia**  
**(Chairman)**

**Hamidah Ibrahim, PhD**  
**Associate Professor**  
**Faculty of Graduate Studies**  
**Universiti Putra Malaysia**  
**(Internal Examiner)**

**Ramlan Mohamad, PhD**  
**Associate Professor**  
**Faculty of Graduate Studies**  
**Universiti Putra Malaysia**  
**(Internal Examiner)**

**Kasmiran Jumari, PhD**  
**Professor**  
**Faculty of Graduate Studies**  
**Universiti Putra Malaysia**  
**(External Examiner)**

---

**HASANAH MOHD GHAZALI, PhD**  
**Professor/ Deputy Dean**  
**School of Graduate Studies**  
**Universiti Putra Malaysia**

**Date:**

**This thesis submitted to the Senate of Universiti Putra Malaysia and has been accepted as fulfillment of the requirement for the degree of Doctor of Philosophy. The members of the Supervisory Committee are as follows:**

**Mohamed Othman, PhD**  
**Associate Professor**  
**Faculty of Computer Science and Information Technology**  
**Universiti Putra Malaysia**  
**(Chairman)**

**Shamala Subramaniam, PhD**  
**Lecturer**  
**Faculty of Computer Science and Information Technology**  
**Universiti Putra Malaysia**  
**(Member)**

**Jalil Md. Desa, PhD**  
**Lecturer**  
**Faculty of Computer Science and Information Technology**  
**Universiti Putra Malaysia**  
**(Member)**

---

**AINI IDERIS, PhD**  
**Professor/Dean**  
**School of Graduate Studies**  
**Universiti Putra Malaysia**

**Date:**

## **DECLARATION**

**I hereby declare that the thesis is based on my original work except for quotations and citations which have been duly acknowledged. I also declare that it has not been previously or concurrently submitted for any other degree at UPM or other institutions.**

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**MOHAMED AWAD ELSHAIKH**

**Date:**

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## **LIST OF ABBRIVIATION**

<b>ACE</b>	<b>Accelerated</b>
<b>AF</b>	<b>Assured Forwarding</b>
<b>AUX</b>	<b>Auxiliary</b>
<b>BA Classifier</b>	<b>Behavior Aggregate Classifier</b>
<b>BE</b>	<b>Best Effort</b>
<b>CBS</b>	<b>Committed Bursty Size</b>
<b>CIR</b>	<b>Committed Information Rate</b>
<b>CIR_th</b>	<b>Committed Information Rate_threshold</b>
<b>DiffServ</b>	<b>Differentiated Services</b>
<b>DSCP</b>	<b>Differentiated Services Code Point</b>
<b>EBS</b>	<b>Excess Bursty Size</b>
<b>EF</b>	<b>Expedited Forwarding</b>
<b>FI</b>	<b>Fairness Index</b>
<b>FWFQ</b>	<b>Fair Weighted Fair Queue</b>
<b>GPS</b>	<b>Generalize Processor Sharing</b>
<b>HoL</b>	<b>Head of Line</b>
<b>HoL_PJ</b>	<b>Head of Line with Priority Jumping</b>
<b>I<sup>2</sup>tswTCM</b>	<b>Double Improve Time Sliding Window Three Color</b>
<b>IETF</b>	<b>M<del>arket</del> Engineering Task Force</b>
<b>IID</b>	<b>Independent and Identically Distribution</b>
<b>ItswTCM</b>	<b>Improve Time Sliding Window Three Color Marker</b>
<b>MF Classifier</b>	<b>Multi-Field Classifier</b>

<b>MIR</b>	<b>Maximum Information Rate</b>
<b>MIR_th</b>	<b>Maximum Information Rate_threshold</b>
<b>MLT</b>	<b>Maximum Laxity Threshold</b>
<b>PBS</b>	<b>Peak Bursty Size</b>
<b>PEC</b>	<b>Performance Evaluation Checklist</b>
<b>PHB</b>	<b>Per Hob Behaviour</b>
<b>PIR</b>	<b>Peak Information Rate</b>
<b>PQ</b>	<b>Priority Queuing</b>
<b>PQWRR</b>	<b>Priority Queue Weighted Round Robin</b>
<b>QoS</b>	<b>Quality of Services</b>
<b>RAS</b>	<b>Rate Adaptive Shaper</b>
<b>RED</b>	<b>Random Early Detection</b>
<b>RSVP</b>	<b>Resource Reservation Protocol</b>
<b>RTT</b>	<b>Round Trip Time</b>
<b>SLA</b>	<b>Service Level Agreement</b>
<b>SLS</b>	<b>Service Level Specification</b>
<b>srRAS</b>	<b>Single Rate - Rate Adaptive Shaper</b>
<b>srTCM</b>	<b>Single Rate Three Color Marker</b>
<b>TD<sup>2</sup>FQ</b>	<b>Token Driven Delay-Sensitive Dynamic Fair Queuing</b>
<b>ToS</b>	<b>Type of Service</b>
<b>trTCM</b>	<b>Two Rate Three Color Marker</b>
<b>trRAS</b>	<b>Two Rate – Rate Adaptive Shaper</b>
<b>tswTCM</b>	<b>Time Sliding Window Three Color Marker</b>

<b>VC</b>	<b>Virtual Clock</b>
<b>WFQ</b>	<b>Weighted Fair Queue</b>
<b>WIRR</b>	<b>Weighted Interleave Round Robin</b>
<b>WRR</b>	<b>Weighted Round Robin</b>